NRF24L01

# NRF24L01 Pinout

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# Implementation with ATMega16/32

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| **nRF24l01** | **ATMega16** | **Description** |
| SCK | PB7 | SPI Clock signal |
| MISO | PB6 | SPI Master in, slave out |
| MOSI | PB5 | SPI Master out, slave in |
| CSN | PB4 | SPI Chip Select |
| CE | PB3 | nrF24l01 Chip Enable, activate Rx and Tx mode |
| IRQ | PD2 (INT0) | Interrupt pin active low |

# Schematics Diagram to be used with example code

